# General Usage Instructions

1. Enter the memory size to be simulated  
     
   Note: maximum memory size is 20, minimum is 1; If the entered number is out of range, 20 will be used.
2. Enter the length of the reference string that is to be generated

Note: minimum is 1, if less is entered, 20 will be used

1. Enter the maximum Page Number to be referenced in the reference string  
     
   Note: minimum is 1, maximum is 99; if the entered number is out of range, 100 will be used

# Output Format

For every scheduling algorithm, the output will contain the following

1. A starting decorator
2. Memory status for each page referenced containing the following
   1. The page being referenced
      1. for second chance, it is also shown whether it is a read-only or read-write (modify) operation
   2. Memory After Reference
   3. Statuses of other Variables/Queues/Bits maintained by the specific scheduling algorithm as following:
      1. FIFO: Queue Front
      2. LRU: None
      3. LFU: Specific frequencies for each page in the memory
      4. Second Chance: Reference Bits, Full Queue
      5. Enhanced Second Chance: Reference Bits, Mod Bits
      6. Optimal: None
3. An End Decorator

At the end of the output, each method’s miss number is shown serially for easier reading.